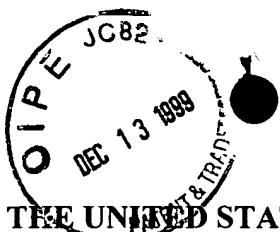


PATENT



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DOCKET NO. 30585/3

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): John S. Yates, Jr. et al.

Serial No.: 09/385,394

Filed: August 30, 1999

Title: COMPUTER FOR EXECUTING TWO DIFFERENT INSTRUCTION SETS
Group 2700

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DEC 15 1999

Art Unit: 2751

Examiner: unassigned

ASSISTANT COMMISSIONER FOR PATENTS
Washington, D.C. 20231

INFORMATION DISCLOSURE STATEMENT

In accordance with 37 C.F.R. §§1.56, 1.97 and 1.98, Applicant wishes to make of record the enclosed items, as listed on the accompanying Form PTO-1449. Applicant respectfully requests the Examiner to fully consider the items and independently ascertain their teaching before issuance of the next action, and to make them of record in the file. The Examiner is also requested to initial and return a copy of the enclosed Form PTO-1449 to evidence such consideration.

The most pertinent portion of the Baxter '602 patent is believed to be Fig. 103, Fig. 110, col. 212, and cols. 345-358.

The VAX 11/780 computer, introduced by Digital Equipment Corp. in the late 1970's, had some capability to execute programs for a first computer architecture on a computer of a second architecture.

Since the early 1960's, IBM has offered emulation and simulation modes whereby its computers could execute program binaries for earlier IBM processors.

It is not believed that the capabilities of any of these prior computers relate to the inventions claimed in this application; nonetheless, the examiner's attention is drawn to these machines as possibly relevant prior art.

Any copy of the items listed on the enclosed Form PTO-1449 that is not enclosed with this Information Disclosure Statement was previously submitted to the Patent and Trademark Office in an Information Disclosure Statement filed in application Serial No. 09/239,194, Yates

I certify that this correspondence, along with any documents referenced therein, is being deposited with the United States Postal Service on December 8, 1999 with sufficient postage as First Class Mail, addressed to The Assistant Commissioner of Patents, Washington D.C. 20231.

et al., EXECUTING PROGRAMS FOR A FIRST COMPUTER ARCHITECTURE ON A COMPUTER OF A SECOND ARCHITECTURE, filed January 28, 1999.

Other items, not listed on the enclosed Form PTO-1449, have been submitted to the Patent and Trademark Office in application Serial No. 09/322,443, Reese, et al., PROFILING OF COMPUTER PROGRAMS EXECUTING IN VIRTUAL MEMORY SYSTEMS, filed ~~DEC 15 1999~~ 28, 1999, and application Serial No. 09/348,317, Yates et al., RECORDING CLASSIFICATION OF INSTRUCTIONS EXECUTED BY A COMPUTER. It is believed that none of the references made of record in the '443 or '317 application are pertinent to the independent claims of the current application, except those that are made of record in this application and listed in this Form 1449. Nonetheless, the Examiner is requested to be aware of these additional items.

No fee is due under 37 C.F.R. §1.17(p) for this Information Disclosure Statement since it is being filed in compliance with 37 C.F.R. §1.97(b)(3), to the knowledge of the undersigned, before the mailing date of a first Office Action on the merits.

The Assistant Commissioner is hereby authorized to charge any additional fees that may be required for this Information Disclosure Statement, or credit any overpayment, to Deposit Account 50-0324, Order No. 30585/3.

Respectfully submitted,

SHEARMAN & STERLING

Dated: December 8, 1999

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